

# 1N4933GP - 1N4937GP

### **Features**

- Low forward voltage drop.
- High surge current capability.
- High reliability.
- High current capability.



DO-41
COLOR BAND DENOTES CATHODE

# Fast Rectifiers (Glass Passivated)

## Absolute Maximum Ratings\*

T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value			Units		
		4933G	4934	4935	4936	4937	1
$V_{RRM}$	Maximum Repetitive Reverse Voltage	50	100	200	400	600	V
I <sub>F(AV)</sub>	Average Rectified Forward Current, .375 " lead length @ T <sub>A</sub> = 75°C	1.0			А		
I <sub>FSM</sub>	Non-repetitive Peak Forward Surge Current 8.3 ms Single Half-Sine-Wave	30		А			
T <sub>stg</sub>	Storage Temperature Range	-65 to +175		°C			
TJ	Operating Junction Temperature -65 to +175			°C			

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

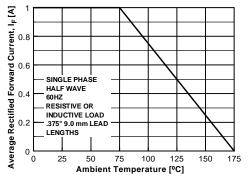
### **Thermal Characteristics**

Symbol	Parameter	Value	Units
P <sub>D</sub>	Power Dissipation	2.73	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	55	°C/W

# **Electrical Characteristics** T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Device			Units		
		4933G	4934	4935	4936	4937	
$V_{F}$	Forward Voltage @ 1.0 A			1.2			V
t <sub>rr</sub>	Reverse Recovery Time $I_F = 0.5 \text{ A}$ , $I_R = 1.0 \text{ A}$ , $I_{rr} = 0.25 \text{ A}$	150			ns		
I <sub>R</sub>	Reverse Current @ rated $V_R$ $T_A = 25^{\circ}C$ $T_A = 125^{\circ}C$	5.0 100		μA μA			
Ст	Total Capacitance V <sub>R</sub> = 4.0 V, f = 1.0 MHz	15		pF			

## **Typical Characteristics**



**Figure 1. Forward Current Derating Curve** 

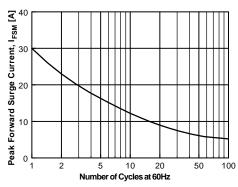


Figure 3. Non-Repetitive Surge Current

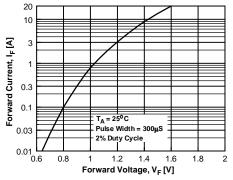


Figure 2. Forward Voltage Characteristics **Reverse Characteristics** 

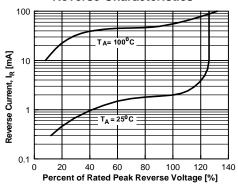


Figure 4. Reverse Current vs Reverse Voltage

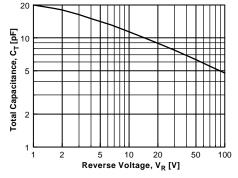
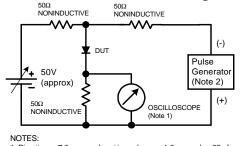
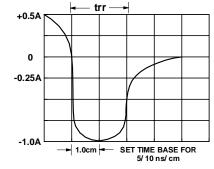


Figure 5. Total Capacitance



- 1. Rise time = 7.0 ns max; Input impedance = 1.0 megaohm 22 pf. 2. Rise time = 10 ns max; Source impedance = 50 ohms.



**Reverse Recovery Time Characterstic and Test Circuit Diagram** 

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